



SHEET 1 OF 3

<b>FORM PTO - 14</b> <b>INFORMATION DISCLOSURE STATEMENT</b>	<b>ATTORNEY DOCKET NO.:</b> MLB-066C2 <b>APPLICANT(S):</b> Griffith et al. <b>SERIAL NO.:</b> 10/646,146 <b>FILING DATE:</b> August 22, 2003 <b>GROUP:</b> Not Yet Assigned
---	--

**U.S. PATENT DOCUMENTS**

EXAM. INIT.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
CL	A1	4,298,448	11/03/81	Muller et al.		
	A2	5,066,559	11/19/91	Elmasry et al.		
	A3	5,380,362	01/10/95	Schubert		
	A4	5,559,057	09/24/96	Goldstein		
	A5	5,587,111	12/24/96	Watanabe et al.		
↓	A6	5,751,018	05/12/98	Alivisatos et al.		

**FOREIGN PATENT DOCUMENTS**

EXAM. INIT.	DOCUMENT NUMBER	DATE	COUNTRY CODE	CLASS	SUB CLASS	FILING DATE	ABSTRACT ONLY	ENGLISH LANG (Y/N)
CL ✓	B1	WO 97/38810	10/23/97	PCT			N	Y

**OTHER ART, JOURNAL ARTICLES, ETC.**

EXAM. INIT.	OTHER DOCUMENTS: (Including Author, Title, Date, Relevant Pages, Place of Publication)	
CL✓	C1	Guo et al., "A Room-Temperature Silicon Single-Electron Metal-Oxide-Semiconductor Memory With Nanoscale Floating-Gate and Ultranarrow Channel," Appl. Phys. Lett. 70 (7), pp. 850-852, February 17, 1997.
✓	C2	Zhuang et al., "Silicon Single-Electron Quantum-Dot Transistor Switch Operating at Room Temperature," Applied Physics Letters, Vol. 72, No. 10, pp. 1205-1207, March 9, 1998.
✓	C3	Guo et al., "Fabrication and Characterization of Room Temperature Silicon Single Electron Memory," J. Vac. Sci. Technol. B 15 (6), pp. 2840-2843, Nov./Dec. 1997.
✓	C4	Yano et al., "SA 21.7:A 128 Mb Early Prototype for Gigascale Single-Electron Memories," IEEE International Solid-State Circuits Conference, 1998.
✓	C5	Ishii et al., "Verify: Key to the Stable Single-Electron-Memory Operation," IEEE, IEDM 97, pp. 171-174, 1997.
✓	C6	Yano et al., "FP 16.4: Single-Electron-Memory Integrated Circuit for Giga-to-Tera Bit Storage," IEEE International Solid-State Circuits Conference, 1996.
✓	C7	Nakajima et al., "Room Temperature Operation of Si Single-Electron Memory with Self-Aligned Floating Dot Gate," IEDM 96, pp. 952-954, 1996.
↓	C8	Tiwari et al., "Technology and Power-Speed Trade-Offs in Quantum-Dot and Nano-Crystal Memory Devices," Symposium on VLSI Technology Digest of Technical Papers, pp. 133-134, 1997.



FORM PTO - 1349 INFORMATION DISCLOSURE STATEMENT		ATTORNEY DOCKET NO.: MLB-066C2  APPLICANT(S): Griffith et al.  SERIAL NO.: 10/646,146  FILING DATE: August 22, 2003 GROUP: Not Yet Assigned
CL ✓	C9	Bhyrappa et al., "Hydrogen-Bonded Porphyrinic Solids: Supramolecular Networks of Octahydroxy Porphyrins," J. Am. Chem. Soc., Vol. 119, 8492-8502, 1997.
✓	C10	Zollinger, <u>Color Chemistry</u> , 2 <sup>nd</sup> Ed., Weinheim, New York, pp. 352-361, 1991.
✓	C11	Murray et al., "Self-Organization of CdSe Nanocrystallites Into Three-Dimensional Quantum Dot Superlattices," Science, Vol. 270, pp. 1335-1338, November 24, 1995.
✓	C12	Terris et al., "Near-Field Optical Data Storage Using a Solid Immersion Lens," Appl. Phys. Lett., Vol. 65, No. 4, pp. 388-390, July 25, 1994.
✓	C13	Markovich et al., "Parallel Fabrication and Single-Electron Charging of Devices Based on Ordered, Two-Dimensional Phases of Organically Functionalized Metal Nanocrystals," Appl. Phys. Lett. 70 (23), pp. 3170-3109, June 9, 1997.
✓	C14	Yoo et al., "Scanning Single-Electron Transistor Microscopy: Imaging Individual Charges," Science, Vol. 276, pp. 579-582, April 25, 1997.
✓	C15	Schoelkopf et al., "The Radio-Frequency Single-Electron Transistor (RF-SET): A Fast and Ultrasensitive Electrometer," Science, Vol. 280, pp. 1238-1242, May 22, 1998.
✓	C16	Shum et al., "A Concept for Nonvolatile Memories," Appl. Phys. Lett., 71 (17), pp. 2487-2489, October 27, 1997.
✓	C17	Tayaoka et al., "Preparation of Co-Fe-P Amorphous Fine Needles With Anodization Technique and Measurement of Demagnetizing Factor," J. Appl. Phys. 79 (8), pp. 6016-6018, April 15, 1996.
✓	C18	Meier et al., "Magnetic Properties of Nanosized Wires," J. Appl. Phys. 79 (8), pp. 6010-6012, April 15, 1996.
✓	C19	Lafdi et al., "Cobalt-Doped Carbon Nanotubes: Preparation, Texture, and Magnetic Properties," J. Appl. Phys. 79 (8), pp. 6007-6009, April 15, 1996.
✓	C20	Mamin et al., "High-Density Data Storage Using Proximal Probe Techniques," IBM J. Res. Develop., Vol. 39, No. 6, pp. 681-699, November 6, 1995.
✓	C21	Masumoto et al., "Room Temperature Operation of a Single Electron Transistor Made by the Scanning Tunneling Microscope Nanooxidation Process for the TiOx/Ti System," Appl. Phys. Lett., 68 (1), pp. 34-36, January 1, 1996.
✓	C22	Terris et al., "Data Storage in NOS: Lifetime and Carrier-to-Noise Measurements," IEEE Transactions on Electron Devices, Vol. 42, No. 5, pp. 944-949, May 5, 1995.
✓	C23	Snow et al., "A Metal/Oxide Tunneling Transistor," Appl. Phys. Lett., 72 (23), pp. 3071-3073, June 8, 1998.
✓	C24	Minne et al., "Automated Parallel High-Speed Atomic Force Microscopy," Appl. Phys. Lett., 72 (18), pp. 2340-2342, May 4, 1998.
✓	C25	Celotta et al., "Nanostructure Fabrication Via Laser-Focused Atomic Deposition (Invited)," J. Appl. Phys. 79 (8), pp. 6079-6083, April 15, 1996.
✓	C26	Krebs et al., "Perpendicular Transport and Magnetic Properties in Patterned Multilayer Magnetic Microstructures (Invited)," J. Appl. Phys. 79 (8), pp. 6084-6089, April 15, 1996.



FORM PTO - 1449		ATTORNEY DOCKET NO.: MLB-066C2
INFORMATION DISCLOSURE STATEMENT		APPLICANT(S): Griffith et al.
		SERIAL NO.: 10/646,146
		FILING DATE: August 22, 2003 GROUP: Not Yet Assigned
CL ✓	C27	Doudin et al., "Arrays of Multilayered Nanowires (Invited)," J. Appl. Phys. 79 (8), pp. 60909-6094, April 15, 1996.
✓	C28	Gadetsky et al., "Magneto-Optical Recording on Patterned Substrates (Invited)," J. Appl. Phys. 79 (8), pp. 5687-5692, April 15, 1996.
✓	C29	Bessho et al., "Fabricating Nanoscale Magnetic Mounds Using a Scanning Probe Microscope," J. Appl. Phys. 79 (8), pp. 5057-5059, April 15, 1996.
✓	C30	Leslie-Pelecky et al., "Self-Stabilized Magnetic Colloids: Ultrafine CO particles in Polymers," J. Appl. Phys. 79 (8), pp. 5312-5314, April 15, 1996.
✓	C31	Johnson et al., "Finite Size Effects in Nanoscale Tb Particles," J. Appl. Phys. 79 (8), pp. 5299-5301, April 15, 1996.
✓	C32	O'Barr et al., "Preparation and Quantitative Magnetic Studies of Single-Domain Nickel Cylinders," J. Appl. Phys. 79 (8), pp. 5303-5305, April 15, 1996.
✓	C33	Levy et al., "Femtosecond Near-Field Spin Microscopy in Digital Magnetic Heterostructures (Invited)," J. Appl. Phys. 79 (8), pp. 6095-6100, April 15, 1996.
✓	C34	Chou et al., "65 Gbits/in. <sup>2</sup> Quantum Magnetic Disk (Abstract)," J. Appl. Phys. 79 (8), p. 5066, April 15, 1996.
✓	C35	Chou et al., "Nanolithographically Defined Magnetic Structures and Quantum Magnetic Disk (Invited)," J. Appl. Phys. 79 (8), pp. 6101-6106, April 15, 1996.
✓	C36	Alivisatos, "Electrical Studies of Semiconductor-Nanocrystal Colloids," MRS Bulletin, pp. 18-23, February 1998.
✓	C37	Xia et al., "Soft Lithography," Angew. Chem. Int. Ed., Vol. 37, pp. 551-575, 1998.
✓	C38	Rosa et al., Direct Patterning of Surface Quantum Wells With An Atomic Force Microscope," Appl. Phys. Lett., 73 (18), pp. 2684-2686, November 2, 1998.
✓	C39	Schamp et al., "Improved efficiencies in light emitting diodes made with CdSe (CdS) core/shell type nanocrystals and a semiconducting polymer," J. Appl. Phys., 82 (11), pp. 5837-5842, December 1, 1997.
EXAMINER /Cathy Lam/		DATE CONSIDERED 12/06/2006